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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Dinkar Singh et. al.

Serial No.: 10/750,697

Group No.: 2822

Filed: 01/02/2004

Examiner: ZARABIAN, AMIR

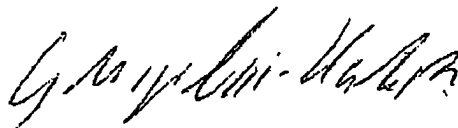
For: ENHANCED T-GATE STRUCTURE FOR MODULATION DOPED FIELD  
EFFECT TRANSISTORS

Commissioner for Patents  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

The attached form 1449 (Modified) gives references pertaining to the above application.

Respectfully,



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Information Disclosure Statement Re: 10/750,697

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ATTENTION EXAMINER: ZARABIAN, AMIR

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Sheet 1 of 1

Form PTO 1449 (modified)				Docket No <u>YCR920010768US2</u>		Serial No <u>10/750,697</u>	
LIST OF PRIOR ART CITED BY APPLICANT				Applicant <u>Dinkar Singh et. al.</u>			
				Filing Date <u>01/02/2004</u>		Group 2822	
U. S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date
	AA	5,290,720	03/01/1994	Chen	438	304	07/26/1993
	AB	5,817,558	10/06/1998	Wu	438	291	12/10/1997
	AC	6,159,781	12/12/2000	Pan	438	167	10/01/1998
	AD	6,417,084	07/09/2002	Singh	438	585	07/20/2000
	AE	6,452,229	09/07/2002	Krivokapic	257	330	02/21/2002
FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date
OTHER PRIOR ART ( Including Author, Title, Date, Pertinent Pages, Etc.)							
Examiner				Date Considered			
<p><b>*EXAMINER:</b> Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant</p>							